

**In the claims:**

1. (Cancelled).
2. (Currently Amended) The method of claim [[1]] 52, further comprising performing a pre-defined operation associated with the timer.
3. (Original) The method of claim 2, wherein the operation is pre-defined by the first wireless communication system.
4. (Cancelled).
5. (Cancelled).
6. (Currently Amended) The method of claim [[1]] 52, further comprising:  
starting a plurality of timers defined for use within the first wireless communication system; and  
when returning to the first wireless communication system, estimating the duration of the transition as a function of the plurality of timers.
7. (Cancelled).
8. (Cancelled).
9. (Previously Presented) A method, comprising:  
starting an IS856 Control Channel Supervision Timer defined for use within an IS856 communication system;  
estimating a duration of a transition from the IS856 communication system to an IS2000-1x communication system as a function of the timer;  
attempting to receive a synchronous control channel capsule; and  
transitioning to a network acquisition state when the attempt to receive the synchronous control channel capsule is unsuccessful.

10. (Currently Amended) The method of claim ~~[[7]]~~ 52, wherein the timer comprises a data rate control (DRC) supervision timer, the method further comprising:  
starting a combination timer; and  
when returning to the IS856 system, estimating the duration of the transition as a function of the DRC supervision timer and the combination timer.

11. (Original) The method of claim 10, further comprising:  
restarting a transmitter in response to expiration of the DRC supervision timer;  
and  
transitioning to an inactive state in response to expiration of the combination timer.

12. (Cancelled).

13. (Currently Amended) The processor-readable medium of claim ~~42~~ 53, containing further instructions for performing a pre-defined operation associated with the timer.

14. (Original) The processor-readable medium of claim 13, wherein the operation is pre-defined by the first wireless communication system.

15. (Cancelled).

16. (Cancelled).

17. (Currently Amended) The processor-readable medium of claim ~~42~~ 53, containing further instructions for:  
starting a plurality of timers defined for use within the first wireless communication system; and  
when returning to the first wireless communication system, estimating the duration of the transition as a function of the plurality of timers.

18. (Cancelled).

19. (Cancelled).

20. (Previously Presented) A processor-readable medium containing processor executable instructions for:

starting an IS856 Control Channel Supervision Timer defined for use within an IS856 communication system;

estimating a duration of a transition from the IS856 communication system to an IS2000-1x communication system as a function of the timer;

attempting to receive a synchronous control channel capsule; and

transitioning to a network acquisition state when the attempt to receive the synchronous control channel capsule is unsuccessful.

21. (Currently Amended) The processor-readable medium of claim ~~18~~ 53, wherein the timer comprises a data rate control (DRC) supervision timer, the processor-readable medium containing further instructions for:

starting a combination timer; and

when returning to the IS856 system, estimating the duration of the transition as a function of the DRC supervision timer and the combination timer.

22. (Original) The processor-readable medium of claim 21, containing further instructions for:

restarting a transmitter in response to expiration of the DRC supervision timer;

and

transitioning to an inactive state in response to expiration of the combination timer.

23. (Cancelled).

24. (Currently Amended) The wireless communication device of claim ~~23~~ 54, wherein the interoperation module is configured to estimate the duration of the transition as a function of a plurality of supervision timers.

25. (Cancelled).

26. (Cancelled).

27. (Previously Presented) A wireless communication device comprising:  
first wireless communication system hardware for operating in a first wireless communication system;

second wireless communication system hardware for operating in a second wireless communication system;

an interoperation module to configure the wireless communication device in response to a transition between the first and second wireless communication systems, the interoperation module adapted to:

estimate a duration of the transition as a function of a supervision timer,  
attempt to receive a synchronous control channel capsule; and  
transition to a network acquisition state when the attempt to receive the synchronous control channel capsule is unsuccessful.

28. (Currently Amended) The wireless communication device of claim ~~25~~ 54, wherein the supervision timer is a data rate control (DRC) supervision timer, and wherein the interoperation module is configured to:

start a combination timer; and  
when returning to the IS856 system, estimate the duration of the transition as a function of the DRC supervision timer and the combination timer.

29. (Original) The wireless communication device of claim 28, wherein the interoperation module is configured to:

restart a transmitter in response to expiration of the DRC supervision timer; and  
transition to an inactive state in response to expiration of the combination timer.

30. (Cancelled).

31. (Currently Amended) The apparatus of claim ~~30~~ 55, further comprising means for performing a pre-defined operation associated with the timer.

32. (Original) The apparatus of claim 31, wherein the operation is pre-defined by the first wireless communication system.

33. (Cancelled).

34. (Cancelled).

35. (Currently Amended) The apparatus of claim ~~34~~ 55, further comprising:  
means for starting a plurality of timers defined for use within the first wireless communication system; and  
means for estimating the duration of the transition as a function of the plurality of timers when returning to the first wireless communication system.

36. (Cancelled).

37. (Cancelled).

38. (Previously Presented) An apparatus comprising:  
means for starting an IS856 Control Channel Supervision Timer defined for use within an IS856 communication system;  
means for estimating a duration of a transition from the IS856 communication system to an IS2000-1x communication system as a function of the timer;  
means for attempting to receive a synchronous control channel capsule; and  
means for transitioning to a network acquisition state when the attempt to receive the synchronous control channel capsule is unsuccessful.

39. (Currently Amended) The apparatus of claim ~~36~~ 55, wherein the timer comprises a data rate control (DRC) supervision timer, the apparatus further comprising:  
means for starting a combination timer; and  
means for estimating the duration of the transition as a function of the DRC supervision timer and the combination timer when returning to the IS856 system.

40. (Original) The apparatus of claim 39, further comprising:  
means for restarting a transmitter in response to expiration of the DRC supervision timer; and  
means for transitioning to an inactive state in response to expiration of the combination timer.

41. (Cancelled).

42. (Currently Amended) The system of claim ~~41~~ 56, wherein the processor further executes the instructions to perform a pre-defined operation associated with the timer.

43. (Original) The system of claim 42, wherein the operation is pre-defined by the first wireless communication system.

44. (Cancelled).

45. (Cancelled).

46. (Currently Amended) The system of claim ~~44~~ 56, wherein the processor further executes the instructions to:  
start a plurality of timers defined for use within the first wireless communication system; and  
when returning to the first wireless communication system, estimate the duration of the transition as a function of the plurality of timers.

47. (Cancelled).
48. (Cancelled)
49. (Previously Presented) A system comprising:  
a memory that stores processor-readable instructions; and  
a processor coupled to the memory that executes the instructions to:  
start a timer defined for use within a first wireless communication system;  
estimate a duration of a transition from the first wireless communication  
system to a second wireless communication system as a function of the timer;  
attempt to receive a synchronous control channel capsule; and  
transition to a network acquisition state when the attempt to receive the  
synchronous control channel capsule is unsuccessful.

50. (Currently Amended) The system of claim 47 56, wherein the timer  
comprises a data rate control (DRC) supervision timer, and wherein the processor further  
executes the instructions to:  
start a combination timer; and  
when returning to the IS856 system, estimate the duration of the transition as a  
function of the DRC supervision timer and the combination timer.

51. (Original) The system of claim 50, wherein the processor further executes the  
instructions to:  
restart a transmitter in response to expiration of the DRC supervision timer; and  
transition to an inactive state in response to expiration of the combination timer.

52. (Currently Amended) ~~The A method of claim 8, further~~ comprising:  
starting a timer defined for use within a first wireless communication system;  
estimating duration of transitions between the first wireless communication system and a  
second wireless communication system as a function of the timer;  
attempting to receive a synchronous control channel capsule; and

transitioning to a network acquisition state when the attempt to receive the synchronous control channel capsule is unsuccessful,

wherein the first wireless communication system is an IS856 system and the second wireless communication system is an IS2000-1x system, and  
wherein the timer comprises an IS856 Control Channel Supervision Timer.

53. (Currently Amended) ~~The A~~ processor-readable medium ~~of claim 19~~, containing ~~further~~ processor executable instructions for:

starting a timer defined for use within a first wireless communication system;  
estimating duration of transitions between the first wireless communication system and a second wireless communication system as a function of the timer;

attempting to receive a synchronous control channel capsule; and  
transitioning to a network acquisition state when the attempt to receive the synchronous control channel capsule is unsuccessful,

wherein the first wireless communication system is an IS856 system and the second wireless communication system is an IS2000-1x system, and  
wherein the timer comprises an IS856 Control Channel Supervision Timer.

54. (Currently Amended) ~~The A~~ wireless communication device ~~comprising of claim 26~~, ~~wherein the interoperation module is configured to:~~

first wireless communication system hardware for operating in a first wireless communication system;

second wireless communication system hardware for operating in a second wireless communication system; and

an interoperation module to configure the wireless communication device in response to a transition between the first and second wireless communication systems, the interoperation module configured to estimate a duration of the transition as a function of a supervision timer,  
wherein the interoperation module is configure to:

attempt to receive a synchronous control channel capsule; and  
transition to a network acquisition state when the attempt to receive the synchronous control channel capsule is unsuccessful, and



wherein the first wireless communication system is an IS856 system and the second wireless communication system is an IS2000-1x system, and  
wherein the supervision timer is a Control Channel Supervision Timer.

55. (Currently Amended) ~~The~~ An apparatus ~~of claim 37, further~~ comprising:  
means for starting a timer defined for use within a first wireless communication system;  
means for estimating duration of transitions between the first wireless communication system and a second wireless communication system as a function of the timer;  
means for attempting to receive a synchronous control channel capsule; and  
means for transitioning to a network acquisition state when the attempt to receive the synchronous control channel capsule is unsuccessful,  
wherein the first wireless communication system is an IS856 system and the second wireless communication system is an IS2000-1x system, and  
wherein the timer comprises an IS856 Control Channel Supervision Timer.

56. (Currently Amended) ~~The~~ A system ~~comprising of claim 48, wherein the processor further executes the instructions to:~~  
a memory that stores processor-readable instructions; and  
a processor coupled to the memory that executes the instructions to start a timer defined for use within a first wireless communication system and to estimate duration of transitions between the first wireless communication system and a second wireless communication system as a function of the timer, wherein the processor further executes instructions to:  
attempt to receive a synchronous control channel capsule; and  
transition to a network acquisition state when the attempt to receive the synchronous control channel capsule is unsuccessful,  
wherein the first wireless communication system is an IS856 system and the second wireless communication system is an IS2000-1x system, and  
wherein the timer comprises an IS856 Control Channel Supervision Timer.